Form PTO 1449
(Rev. 2-32)U.S. Department of Commerce
Patent and Trademark OfficeAtty. Docket No.
FOV-011Serial No.
09/121,175

Information Disclosure Statement by Applicant

Applicant: Richard B. Merrill, et al.

(Use several sheets if necessary)

Filed: 7/22/98

Group: **2700**

U.S. Patent Documents

Init.	Document No.	Date	Name	Class	Subclass	Filing Date
y	4,901,129	2/13/90	Hynecek	357	30	3/23/89
y	4,942,473	07/17/90	Zeevi, et al.	358	213.26	6/30/88
y	5,014,107	5/7/91	Vora	357	44	8/29/89
y	5,021,853	6/4/91	Mistry	357	23.13	4/27/90
y	5,055,418	10/8/91	Vora	437	31	11/13/90
y	5,117,292	5/26/92	Matsunaga	358	213.19	4/27/90
y	5,161,024	11/3/92	Oishi	358	213.24	5/10/91
y	5,276,521	1/4/94	Mori	358	213.31	12/30/92
y	5,317,174	5/31/94	Hynecek	257	222	2/19/93

Foreign Documents

Translation

Init.	Document No.	Date	Country	Class	Subclass	Yes	No

Other Documents (Including Author, Title, Date, Pertinent Pages, Etc.)

y	Bernwald Rossler "Electrically Erasable and Reprogrammable Read-Only Memory Using the n-Channel SIMOS One-Transistor Cell" IEEE Transactions on Electron Devices Vol ED-24, No. 5 pp 606-610 May 1977
y	Carver A. Mead, et al. "Scanners for Visualizing Activity of Analog VLSI Circuitry" California Institute of Technology Computation and Neural Systems Program pp 1-29 July 5, 1991
y	Eric R. Fossum "Active Pixel Sensors: Are CCD's Dinosaurs?" SPIE Vol. 1900 pp.2-14
y	Hon-Sum Philip Wong, et al. "CMOS Active Pixel Image Sensors Fabricated Using a 1.8-V, 0.25-um CMOS Technology pp 889-894 IEEE Transactions on Electron Devices Vol 45 No. 4 April 1998

Examiner

Date considered

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U.S. Patent Documents

Init.	Document No.	Date	Name	Class	Subclass	Filing Date
y	5,335,015	8/2/94	Cooper, et al.	348	302	10/30/92
y	5,341,008	8/23/94	Hynecek	257	231	9/21/93
y	5,396,289	3/7/95	Nakamura	348	294	7/6/93
y	5,414,464	3/9/95	Sasaki	348	222	4/5/94
y	5,414,683	5/9/95	Tani	36.9	47	10/28/92
y	5,424,223	6/13/95	Hynecek	437	3	6/6/94
y	5,428,390	6/27/95	Cooper, et al.	348	240	1/21/94
y	5,471,245	11/28/95	Cooper, et al.	348	302	5/19/94
y	5,541,402	7/30/96	Ackland, et al.	250	208.1	10/17/94

Foreign Documents

Translation

Init.	Document No.	Date	Country	Class	Subclass	Yes	No

Other Documents (Including Author, Title, Date, Pertinent Pages, Etc.)

y	Hon-Sum Philip Wong "CMOS Image Sensors - Recent Advances and Device Scaling Considerations" IEEE pp. 201-204 1997
y	Orly Yadid-Pecht, et al. "A Random Access Photodiode Array for Intelligent Image Capture" IEEE Transactions on Electron Devices, Vol. 38 No 8 pp 1772-1780 August 1991
y	R. Daniel McGrath, et al. "FA 11.2: Current-Mediated, Current-Reset 768x512 Active Pixel Sensor Array" IEEE /Slide Supplement pp 182-183 & 138-139
y	S. Decker et, al. "A 256x256 CMOS Imaging Array with Wide Dynamic Range Pixels and Column-Parallel Digital Output" IEEE International Solid-State Circuits Conference pp.176-177 1998

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Filed: 7/22/98 **Received** Group:

U.S. Patent Documents

OCT 26 1998

Init.	Document No.	Date	Name	Class	Subclass	Filing Date
y	5,541,654	7/30/96	Roberts	348	301	6/8/95
y	5,547,881	8/20/96	Wang, et al.	437	24	3/6/96
y	5,572,074	11/5/96	Standley	307	117	6/6/95
y	5,576,763	11/19/96	Ackland, et al.	348	308	11/22/94
y	5,589,423	12/31/96	White, et al.	437	228	10/3/94
y	5,625,210	4/29/97	Lee et, al.	257	292	4/13/95
y	5,631,704	5/20/97	Dickinson, et al.	348	308	10/14/94
y	5,705,441	1/6/98	Wang, et al.	438	384	3/19/96
y	5,712,682	1/27/98	Hannah	348	255	12/2/96

Foreign Documents

Translation

Init.	Document No.	Date	Country	Class	Subclass	Yes	No

Other Documents (Including Author, Title, Date, Pertinent Pages, Etc.)

y	R. Panicacci, et al. "1/4-Inch CMOS Active Pixel Sensor with Smart On-Chip Functions and Full Digital Interface" Hot Chips IX pp. 41-54 August 25-26 1997
y	S.M. SZE "Physics of Semiconductor Devices" Wiley-Interscience pp 526-533 1969
y	Stephen John Decker "A Wide Dynamic Range CMOS Imager With Parallel On-Chip Analog-to-Digital Conversion" pp 1-205 1997
y	Sunetra K. Mendis et, al "A 128 x 128 CMOS Active Pixel Image Sensor for Highly Integrated Imaging Systems" IEEE pp.583-586 1993

Examiner

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7/22/03

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Group:

U.S. Patent Documents

Init.	Document No.	Date	Name	Class	Subclass	Filing Date
W	5,739,562	4/14/98	Ackland, et al.	257	291	08/01/95

Received
OCT 26 1998
Group 2700

Foreign Documents

Translation

Init.	Document No.	Date	Country	Class	Subclass	Yes	No

Other Documents (Including Author, Title, Date, Pertinent Pages, Etc.)

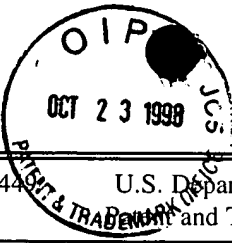
W	T. Delbruck et, al "Analog VLSI Phototransduction by continuous-time, adaptive, logarithmic photoreceptor circuits" California Institute of Technology Computation and Neural Systems Program CNS Memo No. 30 April 2, 1996 pp: 1-24
W	Tobi Delbruck, et al. "Analog VLSI Adaptive, Logarithmic, Wide-Dynamic-Range Photoreceptor" ISCAS '94 Proceedings of the Int'l Symposium on Circuits and Systems IEEE London, May 1994
W	Tatsuo Nomoto, et al. "FA 11.4: A 4M-Pixel Image Sensor with Block and step Access Capability" IEEE International pp.186-187 Plus Four Additional Slides 1997
W	Yoshiaki Hagiwara, Member, IEEE "High-Density and High-Quality Frame Transfer CCD Imager with Very Low Smear, Low Dark Current, and Very High Blue Sensitivity" IEEE Transactions on Electron Devices, Vol. 43 No. 12 pp.2122-2130 December 1996

Examiner

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7/22/02

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Form PTO 1440 U.S. Department of Commerce
(Rev. 2-32) Patent and Trademark OfficeAtty. Docket No.
FOV-011Serial No.
09/121,175

Information Disclosure Statement by Applicant

Applicant: Merrill et al.

(Use several sheets if necessary)

Filed: 7/22/98

Group:

U.S. Patent Documents

Init.	Document No.	Date	Name	Class	Subclass	Filing Date
uy	4,499,529	2/12/85	Figueroa	362	283	8/24/83
uy	4,573,077	2/25/86	Imai	358	212	9/4/84
uy	4,626,915	12/2/86	Takatsu	358	213	7/10/84
uy	4,742,238	5/3/88	Sato	250	578	10/1/86
uy	4,843,474	6/27/89	Suzuki	358	213.19	12/30/87
uy	5,434,620	7/18/95	Higuchi et al.	348	308	9/28/93

Foreign Documents

Translation

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uy	S. Decker et, al. "A 256x256 CMOS Imaging Array with Wide Dynamic Range Pixels and Column-Parallel Digital Output" IEEE International Solid-State Circuits Conference pp.176-177 1998

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Information Disclosure Statement by Applicant

Applicant: Richard B. Merrill, et al.

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Other Documents (Including Author, Title, Date, Pertinent Pages, Etc.)

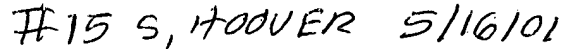
W		R.M. Guidash, et al. "A 0.6 um CMOS Pinned Photodiode Color Imager Technology" IEDM pp.927-929 1997

Examiner

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U.S. Department of Commerce
Patent and Trademark Office

Serial No.

09/121,175

Applicant:

Merrill, et al.

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Filed: 7/22/98

Group: 2712

U.S. Patent Documents

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Foreign Documents

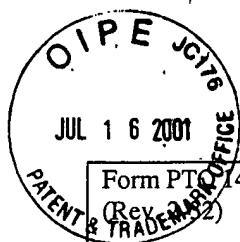
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Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)

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Form PT-1449

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U.S. Department of Commerce
Patent and Trademark OfficeAtty. Docket No.
FOV-011Serial No.
09/121,175

Information Disclosure Statement by Applicant

Applicant: Merrill, et al.

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(Use several sheets if necessary)

Filed: July 22, 1998

Group: 2612

U.S. Patent Documents

Init.		Document No.	Date	Name	Class	Subclass	Filing Date
<i>W</i>	1	3,866,067	2/11/75	Amelio	307	311	5/21/73
<i>W</i>	2	3,971,065	7/20/76	Bayer	358	41	3/05/75
<i>W</i>	3	4,011,016	3/08/77	Layne, et al.	356	195	9/23/75
<i>W</i>	4	4,236,760	12/02/80	Haar, et al.	303	6C	3/01/79
<i>W</i>	5	4,238,760	12/09/80	Carr	357	30	10/06/78
<i>W</i>	6	4,309,604	1/05/82	Yoshikawa, et al.	250	226	7/24/79
<i>W</i>	7	4,473,836	9/25/84	Chamberlain	357	30	5/03/82
<i>W</i>	8	4,499,529	2/12/85	Figueroa	362	283	8/24/83
<i>W</i>	9	4,499,590	2/12/85	Bluzer	377	60	1/14/83
<i>W</i>	10	4,573,077	2/25/86	Imai	358	212	9/04/84
<i>W</i>	11	4,613,895	9/23/86	Burkey, et al.	358	41	11/13/78
<i>W</i>	12	4,651,001	3/17/87	Harada, et al.	250	330	12/13/84
<i>W</i>	13	4,677,289	6/30/87	Nozaki, et al.	250	226	11/04/85
<i>W</i>	14	4,704,633	11/03/87	Matsumoto	358	213.27	2/20/86
<i>W</i>	15	4,786,818	11/22/88	Mead, et al.	250	578	11/09/87
<i>W</i>	16	4,839,735	6/13/89	Kyomasu, et al.	358	213.31	12/21/87
<i>W</i>	17	4,845,553	7/04/89	Konomura, et al.	358	98	5/19/88
<i>W</i>	18	5,038,214	8/06/91	Miida	358	213.11	3/10/89
<i>W</i>	19	5,227,887	7/13/93	Dohi, et al.	358	213.27	12/20/91
<i>W</i>	20	5,241,575	8/31/93	Miyatake, et al.	377	60	9/09/92
<i>W</i>	21	5,289,023	2/22/94	Mead	257	291	8/07/92
<i>W</i>	22	5,397,734	3/14/95	Iguchi, et al.	437	70	10/02/92
<i>W</i>	23	5,414,465	5/09/95	Kodama, et al.	348	236	1/03/94
<i>W</i>	24	5,434,620	7/18/95	Higuchi, et al.	348	308	9/28/93

Foreign Documents

Translation

Init.		Document No.	Date	Country	Class	Subclass	Yes	No

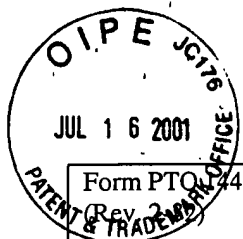
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Form PTO-449

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Patent and Trademark OfficeTechnology Center 2600
Docket No. FOV-011Serial No.
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Information Disclosure Statement by Applicant

Applicant: Merrill, et al.

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Group: 2612

U.S. Patent Documents

Init.		Document No.	Date	Name	Class	Subclass	Filing Date
y	25	5,461,425	10/24/95	Fowler, et al	348	294	2/15/94
y	26	5,502,299	3/26/96	Standley	250	208.2	12/12/94
y	27	5,668,596	9/16/97	Vogel	348	222	2/29/96
y	28	5,742,058	4/21/98	Pantigny, et al.	250	370.08	5/30/96
y	29	5,801,657	9/01/98	Fowler, et al.	341	155	2/05/97
y	30	5,883,421	3/16/99	Chouikha, et al	257	461	3/13/97
y	31	6,046,444	4/04/00	Afghahi	250	208.1	12/08/97
y	32	6,115,066	9/05/00	Gowda, et al.	348	308	6/12/97
y	33	6,211,510 B1	4/03/01	Merrill, et al.	250	208.1	1/26/00
y	34	6,246,436 B1	6/12/01	Lin, et al.	348	308	11/03/97

Foreign Documents

Translation

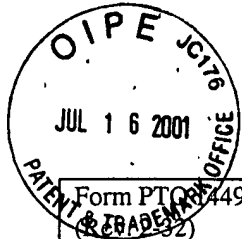
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y	35	0 466 929 A1	22.01.92	EP	H04N 5	238		
y	36	0 605 898 A1	13.07.94	EP	H01L 27	146		
y	37	0 777 379 A1	04.06.97	EP	H04N 3	15		
y	38	0 854 516 A2	22.07.98	EP	H01L 27	146		
y	39	62143571	26.06.87	JP	H04N 5	335		
y	40	01103378	20.04.89	JP	H04N 5	335		
y	41	06133320	13.05.94	JP	H04N 9	09		
y	42	08009391	12.01.96	JP	H04N 9	04		
y	43	08095670	12.04.96	JP	G06F 1	16		
y	44	43 04 506 A1	18.08.94	DE	H04N 5	225		
y	45	198 36 356 A1	12.05.99	DE	H01L 27	1		
y	46	54-108628	25.08.79	JP	G03B 7	20		No
y	47	58-83824	19.05.83	JP	G03B 7	20		No
y	48	59-152424	31.08.84	JP	G03B 7	20		No

Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)

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Applicant: Merrill, et al.

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U.S. Patent Documents

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Foreign Documents

Translation

Init.	Document No.	Date	Country	Class	Subclass	Yes	No
✓	49	59-42527	09.03.84	JP	G03B 17 14		No
✓	50	60-23841	06.02.85	JP	G03B 17 14		No
✓	51	60-53912	28.03.85	JP	G02B 7 11		No
✓	52	99/66560	23.12.99	Int.	H01L 27 144		
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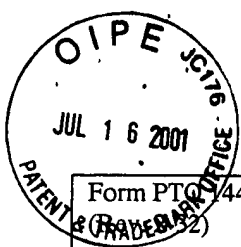
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)

✓	55	Albert J.P. Theuwissen, "Solid-State Imaging with Charge-Coupled Devices", Kluwer Academic Publishers, 1997, Chapter 5, pp. 131-141.
✓	56	Bob Weibel, "High-End Digital Cameras Can Make Professional Indoor Photography A Snap", Buyer's Guide, Published April 1997, pp. 71-78.
✓	57	Chye Huat Aw, et al. "A 128 x 128 Pixel Standard-CMOS Image Sensor With Electronic Shutter", IEEE International Solid-State Circuits Conference, 1996, pp. 4-39 to 4-40.
✓	58	Don Sutherland, "Neaveau Niche-Part I. The Latest in Digital SLRs", Shutterbug, November 1997, pp. 192, 193, 202, 208 & 210.
✓	59	D. Knipp, et al. "Low Cost Approach to Realize Novel Detectors For Color Recognition", Forschungszentrum Julich GmbH, ISI-PV, (year unknown), pp. 350-353.
✓	60	Guang Yang, et al. "A Snap-Shot CMOS Active Pixel Imager For Low-Noise, High-Speed Imaging", IEEE, 1998, pp. 45-48.
✓	61	Hon-Sum Wong, et al. "Technology and Device Scaling Considerations for CMOS Imagers", IEEE Transactions on Electron Devices, Vol. 43 No. 12, December 1996, pp. 2131-2142.
✓	62	Roland A. Anders, et al., "Developmental Solid-State Imaging System", IEEE Transactions on Electron Devices, Vol. ED-15, No. 4, April 1968, pp. 191-261.
✓	63	Ken Parulski, et al. "Enabling Technologies for a Family of Digital Cameras", Eastman Kodak Company, SPIE Vol. 2654, 1996, Invited Paper, pp. 156-163.
✓	64	Mohamed Ben Chouikha, et al. "Buried Triple p-n Junction Structure in a BiCMOS Technology for Color Detection", IEEE BCTM 6.4, 9/1997 pp. 108-111.
✓	65	Mohamed Ben Chouikha, et al. "Color Sensitive Photodetectors in Standard CMOS and BiCMOS Technologies", SPIE Vol. 2950, 1996, pp. 108-120.
✓	66	R.W.G. Hunt, "The Reproduction of Colour", Fountain Press, England, Fifth Edition 1995, pp. 451-481.
✓	67	Savvas G. Chamberlain, "Photosensitivity and Scanning of Silicon Image Detector Arrays" IEEE Journal of Solid-State Circuits, Vol. SC-4, No. 6, December 1969, pp. 333-342.
✓	68	Hiroki Miura, et al., "A 100 Frame/s CMOS Active Pixel Sensor For 3D-Gesture Recognition System", IEEE International Solid-State Circuits Conference, June 1999, pp. 142-143.

Examiner

Date Considered

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(Use several sheets if necessary)						Filed: July 22, 1998		Group: 2612	
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Foreign Documents									
Init.		Document No.	Date	Country	Class	Subclass	Yes	No	
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)									
cy	69	D. Renshaw, et al., "ASIC Image Sensors", IEEE, 1990, pp. 3038-3041.							
y	70	Peter B. Denyer, et al., "On-Chip CMOS Sensors For VLSI Imaging systems", VLSI 91, Proceedings of the IFIP TC 10/WG 10.5 International Conference on Very Large Scale Integration, August 1991, pp. 4b.1.1 to 4b.2.1.							
y	71	J.E.D. Hurwitz, et al. "An 800K-Pixel Color CMOS Sensor For Consumer Still Cameras", SPIE Vol. 3019, 1997, pp. 115-124.							
y	72	Rudolph H. Dyck et al., "Integrated Arrays of Silicon Photodetectors For Image Sensing", IEEE Transactions on Electron Devices, Vol. ED-15, No. 4, April 1968, pp. 196-201.							
y	73	Peter J.W. Noble, "Self-Scanned Silicon Image Detector Arrays", IEEE Transactions on Electron Devices, Vol. ED-15, No. 4, April 1968, pp. 202-208.							
y	74	G. Sadasiv, et al., "Thin-Film Circuits For Scanning Image-Sensor Arrays", IEEE Transactions on Electron Devices, Vol. ED-15, No. 4, April 1968, pp. 215-219.							
y	75	Thierry M. Bernard, "Advanced Focal Plane Arrays and Electronic Cameras", Proceedings EurOpt Series, Vol. 2950, October 1996, pp. 111-120.							
y	76	Akihiro Nitayama, et al., "Future Directions for DRAM Memory Cell Technology", Microelectronics Engineering Laboratory, Toshiba Corp., Japan							
y	77	David X.D. Yang, et al., "A 640 x 512 CMOS Image Sensor With Ultrawide Dynamic Range Floating-Point Pixel-Level ADC", Journal of Solid State Circuit, Vol. 34, No 12, December 1999, pp. 1821-1834.							
rk	78	David X.D. Yang, et al., "A Nyquist Rate Pixel Level ADC for CMOS Image Sensors", IEEE Journal of Solid State Circuits, Vol. 34, No. 3 March 1999, pp. 348-356.							
y	79	David X.D. Yang, et al., "A 128 x 128 Pixel CMOS Area Image Sensor With Multiplexed Pixel Level A/D Conversion", Information Systems Laboratory, Electrical Engineering Dept., Stanford University.							
y	80	Boyd Fowler, et al., "Techniques For Pixel Level Analog to Digital Conversion", Information Systems Laboratory, Stanford University.							
y	81	Boyd Fowler, et al., "A CMOS Area Image Sensor With Pixel Level A/D Conversion", Information Systems Laboratory, Electrical Engineering Department, Stanford University, November 20, 1995.							
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